

### **Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of Claims:**

1. (Currently Amended) A method for reading data from a memory card that provides non-volatile data storage having an address space **defined by a contiguous range of addresses**, said method comprising:

(a) accessing volume information stored in a **range of addresses that is a proper subset of the contiguous range of addresses that defines the address space** ~~portion of a first volume of the address space of the non-volatile data storage~~;

(b) determining whether the non-volatile data storage has a single volume address space or a multiple volume address space based on the volume information;

(c) operating the memory card by accessing the entire address space of the non-volatile data storage as the single volume when said determining (b) determines that the single volume address space is present on the memory card; and

(d) operating the memory card by dividing the address space of the non-volatile data storage into a plurality of volumes when said determining (b) determines that the multiple volume address space is present on the memory card, one of the plurality of volumes being the first volume.

2. (Original) A method as recited in claim 1,

wherein the memory card includes a switch that has a plurality of switch positions, and wherein said operating (d) includes at least:

(d1) determining a switch position for the switch; and

(d2) selectively enabling one of the plurality of volumes based on the switch position.

3. (Original) A method as recited in claim 2,

wherein the switch has at least a first position and a second position, and

wherein said operating (d) further includes at least:

(d3) imposing an address offset when the switch is in the second position.

4. (Original) A method as recited in claim 3, wherein the address offset enables the memory card to provide more data storage capacity than available with a file system using 16-bit addressing.
5. (Currently Amended) A method as recited in claim 2,  
wherein the switch has at least a first position and a second position,  
wherein, when the switch position is in the first position and the memory card is operated by dividing the address space of the non-volatile data storage into the plurality of volumes, the first volume of the non-volatile data storage is accessed, and  
wherein, when the switch position is in the second position and the memory card is operated by dividing the address space of the non-volatile data storage into the plurality of volumes, a second volume of the non-volatile data storage is accessed.
6. (Original) A method as recited in claim 5, wherein the memory card is formatted into either one of a single volume or a pair of volumes, the pair of volumes being the first volume and the second volume.
7. (Original) A method as recited in claim 6, wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the first and second volumes of X/2 gigabytes each as the pair of volumes.
8. (Original) A method as recited in claim 1, wherein said method further comprises:  
(e) detecting activation of the memory card, and  
wherein said accessing (a), said determining (b), and said operating (c) or (d) are performed once said detecting (e) detects the activation of the memory card.
9. (Original) A method as recited in claim 8, wherein the activation of the memory card occurs upon power-on of the memory card or upon insertion of the memory card into a host device.
10. (Original) A method as recited in claim 1,  
wherein the memory card is formatted into a single volume or a plurality of volumes, and  
wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the N volumes of X/N gigabytes each as the plurality of volumes.

11. (Previously Presented) A method as recited in claim 1, wherein when said determining (b) determines that the single volume address space is present on the memory card, the first volume has a FAT-32 file format.

12. (Previously Presented) A method as recited in claim 1, wherein when said determining (b) determines that the multiple volume address space is present on the memory card, each of the multiple volumes has a FAT-16 file format.

13. (Currently Amended) A memory card **having a single memory array defined by a contiguous range of addresses** capable of being configured as a single partition having a first size or as multiple partitions each having a second size, said memory card comprising:

non-volatile data storage that provides data storage having an address space, said address space being configured to include at least a first partition, the first partition including partition information **that is stored in a range of addresses that is a proper subset of the contiguous range of addresses;**

a switch being set in one of a plurality of switch positions; and

a controller that manages access to the data stored in said non-volatile data storage,

wherein said controller is configured to examine the partition information stored in said first partition to determine whether the single partition or the multiple partitions are being used based on the partition information,

wherein when said controller determines that the single partition is used, the entire address space of said non-volatile data storage is accessed as a single partition, the first partition being the single partition, and

wherein when said controller determines that the multiple partitions are being used, the address space of said non-volatile data storage is divided into as multiple partitions, one of the multiple partitions being accessed based on the switch position of said switch.

14. (Original) A memory card as recited in claim 13, wherein the one or more partitions are volumes.

15. (Original) A memory card as recited in claim 13, wherein when said controller determines that the single partition is being used on said memory card, the single partition has a FAT-32 file format.

16. (Original) A memory card as recited in claim 13, wherein when said controller determines that the multiple partitions are being used, each of the multiple partitions has a FAT-16 file format.

17. (Original) A memory card as recited in claim 13,

wherein said memory card is formatted into either a single partition or a plurality of partitions, and

wherein the total non-volatile data storage for said memory card is formatted to a first partition of X gigabytes as the single partition, or formatted into the N partitions of X/N gigabytes each as the plurality of partitions.

18. (Original) A memory card as recited in claim 13, wherein said memory card is a FLASH memory device.

19. (Currently Amended) A memory device that provides non-volatile data storage having an address space **defined by a contiguous range of addresses**, said memory device comprising:

means for accessing volume information **stored in a range of addresses that is a proper subset of the contiguous range of addresses that defines the address space stored in a portion of a first volume of the address space of non-volatile data storage**;

means for determining whether a single volume address space or a multiple volume address space is present on said memory device based on the volume information; and

means for operating said memory device based on a configuration of said memory device by accessing the entire address space of the non-volatile data storage as a single volume, or by dividing the address space of the non-volatile data storage into multiple volumes based on a determination of said means for determining.

20. (Previously Presented) A memory device as recited in claim 19,

wherein said means for operating operates said memory device in a first mode when said means for determining determines that the single volume address space is present on said memory device, and

wherein said means for operating operates said memory device in a second mode when said means for determining determines that the multiple volume address space is present on said memory device.

21. (Original) A memory device as recited in claim 20, wherein said memory device further comprises a switch means that permits user selection of one of the first mode and the second mode.

22. (Previously Presented) A memory device as recited in claim 19, wherein said means for operating operates said memory device as providing only the first volume when said means for determining determines that the single volume address space is present on said memory device.

23. (Previously Presented) A memory device as recited in claim 19, wherein said means for operating operates said memory device as providing a plurality of volumes when said means for determining determines that the multiple volume address space is present on said memory device, one of the plurality of volumes being the first volumes.

24. (Original) A memory device as recited in claim 19, wherein said memory device is a memory card.

25. (Original) A memory device as recited in claim 19, wherein said memory device is a FLASH memory device.

26. – 35. (Canceled).